



REVIEW ARTICLE

A NEW APPROACH TO PASSIVE FILTER DESIGN FOR VOLTAGE SOURCE CONVERTERS

*Amin Farajollahi

University of Azad, Tehran, Iran

ARTICLE INFO

Article History:

Received 19th June, 2017
Received in revised form
24th July, 2017
Accepted 05th August, 2017
Published online 30th September, 2017

Key words:

LCL filter,
Passive filter,
Voltage source converter.

ABSTRACT

Designing passive filter for the voltage source converter is very popular. In this paper a new approach to design a LCL filter is proposed. The step by step design procedure is shown which is missing in most of the papers published recently. The proposed method meets the traditional practical limits, while guarantees more stability even in the presence of wide grid inductance variations and filter parameter uncertainties. The performance of the proposed method are verified by simulation results

Copyright©2017, Amin Farajollahi. This is an open access article distributed under the Creative Commons Attribution License, which permits unrestricted use, distribution, and reproduction in any medium, provided the original work is properly cited.

Citation: Amin Farajollahi, 2017. "A New Approach to Passive Filter Design For Voltage Source Converters", *International Journal of Current Research*, 9, (9), 58015-58018.

INTRODUCTION

Renewable energy is at the centre of the transition to a less carbon-intensive and more sustainable energy system. Renewables have grown rapidly in recent years, accompanied by large cost reductions for solar photovoltaics and wind power in particular (Chitsazan *et al.*, 2017). The increase of power electronics in power systems has begun national and international activities to change standards and regulations concerning harmonics (Mohan *et al.*, 2003). Furthermore, with the developments of power semiconductors and power electronics technologies, grid connected converters is found extremely wide and important applications in modern electric power systems, e.g. photovoltaic inverters for distributed generation, active power filters for power quality enhancement. Moreover, increasing energy demands as well as rising cost and environmental concerns about conventional energy resources have absorbed considerable attention to distributed generation powered by renewable resources. IEEE Std. 519-2014 includes suggested limits on harmonic currents injected by nonlinear loads along with the quality of voltage, a utility can supply to a consumer at any point in a distribution system (Araujo *et al.*, 2007). The limits on harmonic currents is used to design the filters when grid characteristics are unknown (Carrasco *et al.*, 2006). Voltage-source converters (VSCs) injects the generated power in distributed generation systems (Blaabjerg *et al.*, 2006; Wu *et al.*, 2012). In order to reduce the switching harmonics, a low-pass filter is used to connect VSCs to the grid. This filter is customarily a simple inductance. Besides the simple L filter, the higher order filters, for example LCL provide better reduction performance with lesser total inductance, which results in considerable reduction in the volume and the total cost of the filter (Chitsazan *et al.*, 2012; Xu *et al.*, 2014; Li *et al.*, 2017). But, the inherent resonance of high-order filters may affect the stability of the current control loop. To solve the instability problem, different methods such as passive damping, active damping, and delay-based stabilization have been presented (Bloemink, 2011; Tang *et al.*, 2016). As mentioned before, the LCL-filter is a third-order Low Pass Filter considered as an effective switching harmonic attenuation with decreased inductance requirement (Liserre, 2005; Chitsazan, 2016; Pe~na-Alzola, 2013). Consequently, according to achieve high quality sinusoidal grid current to comply with the strict grid code, LCL filter is usually chosen. Several papers have been presented to propose methods to deal with the LCL resonance. The simplest way is to add a damping resistor in series with the capacitor of the LCL-filter which reduces the resonance peaks while controlling the stability of current in a wide range of frequencies.

Although its simplicity and effectiveness, it results in power loss in the system and decrease the efficiency and reliability of passive components. For voltage sourced grid-connected converters, an L or an LCL filter should be used at the output to diminish the harmonic pollution and the pulsed voltages. This filter is usable to converters with low to medium switching frequencies, where the switching sideband frequencies are comparatively close to the fundamental frequency. In this situation, an effective harmonics filtering by the conventional filters may affect the fundamental component. The other potential application, as recommended by (Chitsazan, 2012), is for the converters with a low converter side inductor. In this paper, a new approach to an LCL filter is proposed. First, the stability conditions, by means of the delay-based stabilization method, for the resonance frequencies are reviewed, considering the folding effect of the digital system. Then, the resonance frequencies of the proposed LCL filter are calculated used for the filter parameter design. Subsequently, a filter design procedure, simultaneously considering some practical criteria, is proposed. The proposed filter design algorithm is also robust against a wide range of grid inductance variations and filter elements uncertainties. The theoretical achievements are verified by simulation. The proposed filter is presented in Section II. Filter design procedure is explained in Section III. Simulation of a case study is described in Section IV, and Section V concludes the considerations.

Proposed Filter

Fig. 1 shows a single-phase VSC, which is connected to the grid through a proposed LCL filter. In this filter several LC branches are connected in parallel with the capacitor branch of the conventional LCL filter. The filter combines the advantages of both LCL and tuned trap filters.

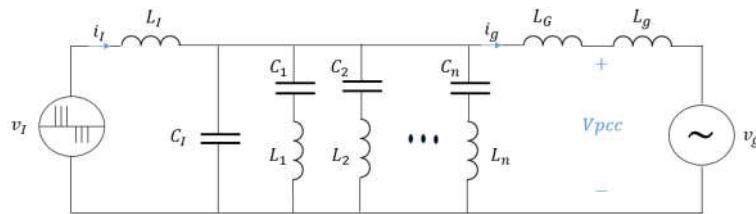


Fig. 1. The proposed filter

In Fig. 1, (L_1, L_G, C_i) and (L_i, C_i) are the parameters of LCL and i_{th} trap, correspondingly. The transfer function of the converter output voltage to the grid current is calculated as

$$G_{ig}(s) = \frac{i_g(s)}{v_1(s)} = \frac{1}{L_1 \cdot (L_G + L_g) \cdot C_i \cdot s} \prod_{i=1}^n \frac{(s^2 + \omega_{f,i}^2)}{(s^2 + \omega_{res,i}^2)} \quad (1)$$

where $\omega_{f,i}$ and $\omega_{res,i}$ are the tuned and resonant frequency of the i_{th} trap respectively.

Filter Design Procedure

Closed-loop stability must be certain without any spare resistors, sensors. Consequently, all resonance frequencies must lie inside the stable region. But, the main problem is the uncertainties due to component tolerances which may cause significant deviations of the resonance frequencies from the design values. Also, the resonance frequencies is kept in the stable range in all situations. As mentioned before, the filter resonance frequencies regulate the stability of the closed-loop system. The resonance frequencies of the proposed filter are derived as a function of the filter parameters. The resonance frequencies of a circuit are independent of the sources and are only determined by the passive elements and their arrangement in the circuit. Hence, for the sake of simplicity all sources are removed from the circuit. The filtered VSC of Fig. 1, without the sources, simplifies to Fig. 2.

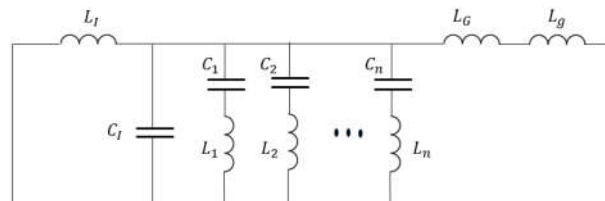


Fig. 2: The simplified circuit of grid-connected convert

To attain a robust filter design in contradiction of the grid inductance variations and filter parameter uncertainties, the stable range of resonance frequency must be met for all resonance frequency variation range determined in previous sections. Also, the designed filter should meet some practical restrictions, such as the converter current ripple and the grid current harmonics. In the following, the step-by-step design procedure of the *MLCL* filter is suggested. For the sake of simplicity, in the design procedure only one trap for the *MLCL* filter is considered. System parameters and design constraints are listed in Table I. It should be mentioned that the maximum converter current ripple is 25% of the rated current. Also uncertainty factor for all elements is considered 50%.

Table 1. System Parameters and Design Constraints

Parameters	Value
Grid Vol./Freq. (V_{rms}/f)	220/60
DC-Link (V_{dc})	400 V
Switch. Freq. (f_{sw})	6 KHz
Sampling Freq. (f_s)	12 KHz
VSC Rated Power (S_{rated})	5 KVA

In the first step, the minimum value of L_1 is determined according to the maximum allowed converter current ripple (Carrasco *et al.*, 2006)

$$L_1 = \frac{V_{dc}}{2.4 I_{rated} \cdot f_s} \tag{2}$$

With the parameters of Table 1, L_1 is calculated as 611.11 μH . Also, the maximum decrease in the resonance frequency value is occurred when L_G is considered as infinite value and the max uncertainty.

The resultant minimum resonance frequency is calculated as

$$f_{res} = \frac{1}{4\pi^2} \sqrt{\frac{1}{50\% \cdot L_1} \left(50\% \cdot C_1 + \frac{C_1}{1 - L_1 C_1 \omega_s^2} \right)} \tag{3}$$

Therefore if $L_1 \cdot C_1$ is replaced by $\frac{1}{\omega_s^2}$, the values of C_1 and C_1 can be written as

$$C_1 = \frac{1}{50\% \cdot 50\% \cdot L_1} \left(\frac{0.82}{f_s} \right)^2 \tag{4}$$

$$C_1 = 0.972 * 0.361 * 50\% * C_1 \tag{5}$$

The minimum value for L_G is as follow (Peña – Alzola *et al.*, 2013)

$$L_2 = \frac{V_{max}}{5.95 \cdot I_{rated} \cdot L_1 \cdot C_1 \cdot f_s^3} \tag{6}$$

Simulation results

Figure 3 shows the case study introduced in (Mohammad Amin Chitsazan, *et al.*, 2017; Mohammad Amin Chitsazan *et al.*, 2017).

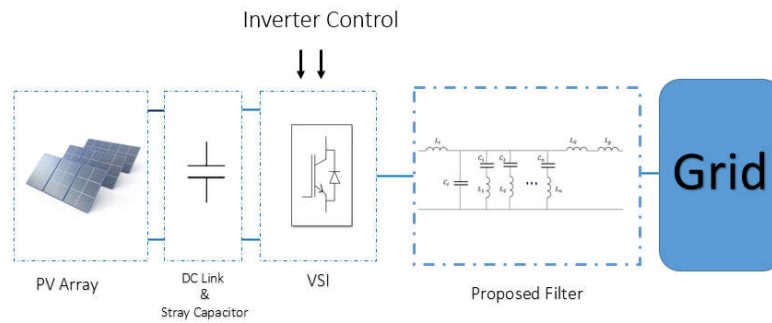


Fig.3. The case study

The parameters are listed in Table.2.

Table. 2. The simulation results

Type	L_1	L_2	L_f	C	C_f
MLCL	656 μH	220 μH	210 μH	4.3 μF	2.3 μF
LCL	656 μH	1345 μH	-	7.5 μF	-

Fig. 4 shows waveforms and FFTs of currents injected into grid by the LCL and MLCL filters. It can be seen that in comparison with the LCL filter the harmonic currents in the MLCL filter have been considerably reduced. Also, the total harmonic distortion in the LCL filter is 4.86%, while in the PST-LCL it decreased to 2.05%.

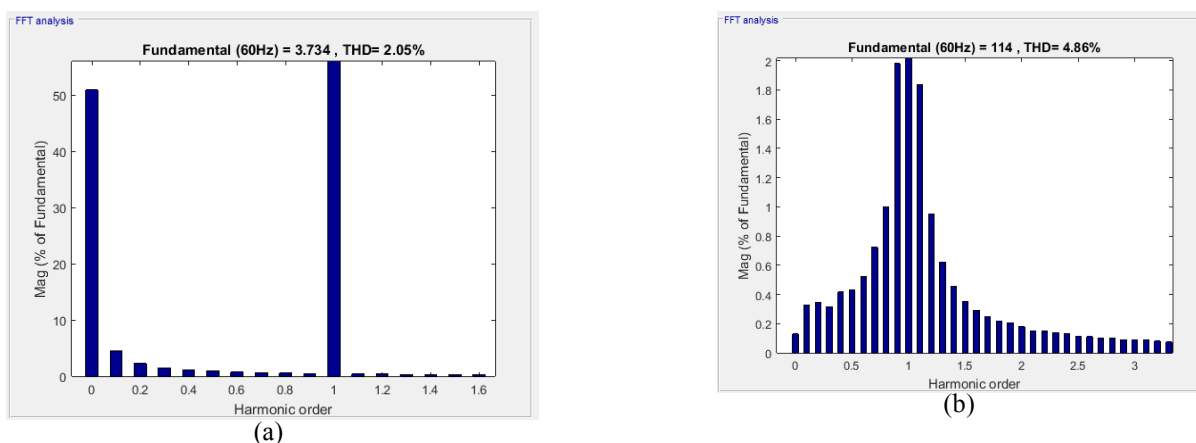


Fig.4. FFT analysis of the case study with a) MLCL, b) LCL filter

Conclusion

A new approach to LCL filter design for grid interconnected inverter systems has been proposed. A trap filter is added to the regular LCL filter, creating a MLCL filter with enhanced performance. Harmonic currents are significantly reduced improving operation of the involved utility grid. The projected fully controllable filter allows mitigating the current THD to a preferred range while allows the system to respond to load and switching frequency changes rapidly. The inclusive design procedure of the MLCL filter has been described in detail.

REFERENCES

- Araujo, S., Engler, A., Sahan, B., Antunes, F. 2007. LCL filter design for grid-connected NPC inverters in offshore wind turbines. In: Power Electronics, 2007. ICPE '07. 7th International Conference on. p. 1133-1138.
- Blaabjerg, F., Teodorescu, R., Liserre, M. and Timbus, A. V. 2006. "Overview of control and grid synchronization for distributed power generation systems," IEEE Trans. Ind. Electron., vol. 53, no. 5, pp. 1398-1409, Oct.
- Bloemink, J.M. and Green, T. C. 2011. "Reducing passive filter sizes with tuned traps for distribution level power electronics," in Proc. 14th Eur. Conf. Power Electron. Appl., Aug./Sep, pp. 1-9.
- Carrasco, J. M., Franquelo, L. G., Bialasiewicz, J. T., Galvan, E., PortilloGuisado, R. C., Prats, M. A. M., Leon, J. I. and Moreno-Alfonso, N. 2006. "Power-electronic systems for the grid integration of renewable energy sources: A survey," IEEE Trans. Ind. Electron., vol. 53, no. 4, pp. 1002-1016, Jun.
- Chitsazan, M. A., Gharehpetian, G., Arbabzadeh, M. 2012. "Application of voltage source converter in interphase power controller, Proc. of World Congress on Engineering and Computer Science (WCECS), vol. 2, pp. 1-6, Oct.
- Chitsazan, M. A., Sami Fadali, M., Amanda K. Nelson, A. and Trzynadlowski, M. 2017 "Wind speed forecasting using an echo state network with nonlinear output functions", American Control Conference (ACC), 2017 IEEE, pp. 5306-5311, May.
- Chitsazan, M. A., Trzynadlowski, A. M. 2016. "Harmonic mitigation in interphase power controllers using passive filter-based phase shifting transformer", Energy Conversion Congress and Exposition (ECCE), 2016 IEEE, pp. 1-5, Sep.
- Li, F., Zhang, X., Zhu, H., Li, H. and Yu, C. 2015. "An LCL-LC filter for grid-connected converter: Topology, parameter, and analysis," IEEE Trans. Power Electron., vol. 30, no. 9, pp. 5067-5077, Sep.
- Liserre, M., Blaabjerg, F. and Hansen, S. "Design and control of an LCL-filter-based three-phase active rectifier," IEEE Trans. Ind. Appl., vol. 41, no. 5, pp. 1281-1291, Sep./Oct.
- Mohammad Amin Chitsazan, Andrzej M. Trzynadlowski, 2017. Harmonic Mitigation in Three-Phase Power Networks with Photovoltaic Energy Sources, American Journal of Electrical Power and Energy Systems. Vol. 6, No. 5, pp. 72-78.
- Mohammad Amin Chitsazan, Andrzej M. Trzynadlowski. 2017. A New Approach to LCL Filter Design for Grid-Connected PV Sources. American Journal of Electrical Power and Energy Systems. Vol. 6, No. 4, pp. 57-63.
- Mohan, N., Undeland, T.M., Robbins, W.P. 2003. Power electronics, Converters, Applications and Design. John Wiley & Sons.
- Peña-Alzola, R., Liserre, M., Blaabjerg, F., Sebastián, R., Dannehl, J. and Fuchs, F. W. 2013. "Analysis of the passive damping losses in LCL filter-based grid converters," IEEE Trans. Power Electron., vol. 28, no. 6, pp. 2642-2646, Jun.
- Tang, Y., Yao, W., Loh, P. C. and Blaabjerg, F. 2005. "Design of LCL filters with LCL resonance frequencies beyond the Nyquist frequency for grid-connected converters," IEEE J. Emerg. Sel. Topics Power Electron., vol. 4, no. 1, pp. 3-14, Mar. 2016.
- Wu, W., He, Y., and Blaabjerg, F. 2012. "An LLCL power filter for single-phase grid-tied inverter," IEEE Trans. Power Electron., vol. 27, no. 2, pp. 782-789, Feb.
- Xu, J., Yang, J., Ye, J., Zhang, Z. and Shen, A. 2014. "An LTCL filter for three-phase grid-connected converters," IEEE Trans. Power Electron., vol. 29, no. 8, pp. 4322-4338, Aug.